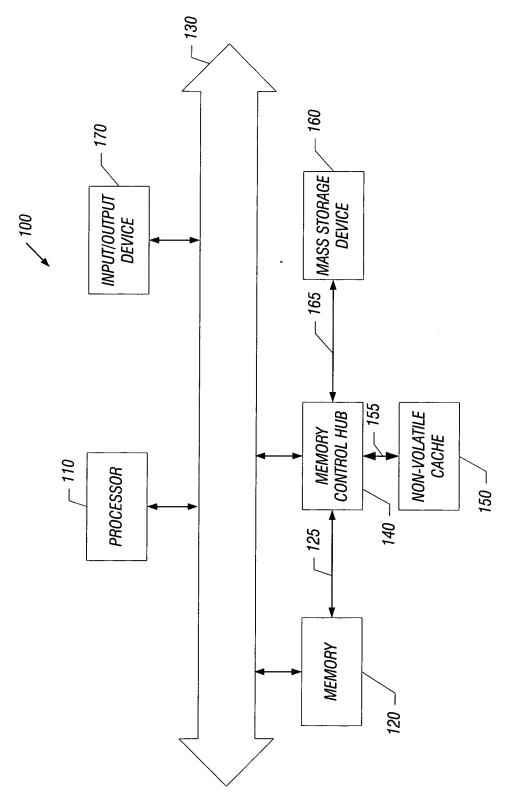


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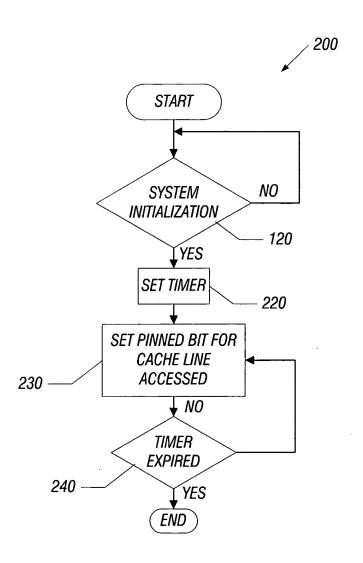


FIG. 2

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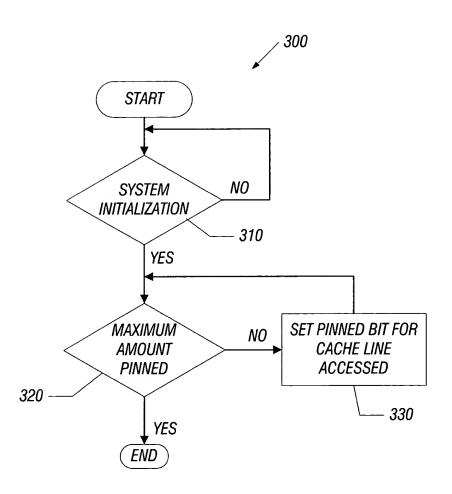


FIG. 3

Richard L. Coulson 09/894,310 Applicant: Serial No.: June 27, 2001 Filing Date: Docket No.: ITL.1228US (P11448) Sheet 4 of 4 400 **START SYSTEM** NO INITIALIZATION 410 YES **SET TIMER** 420 SET PINNED BIT FOR NO MAXIMUM **CURRENTLY ACCESSED** AMOUNT PINNED **CACHE LINE** 430 -YES 440 -*CURRENTLY* NO ACCESSED CACHE LINE CACHED BEFORE **REBOOT** 450 480 -YES **BRING CURRENTLY** EXISTING NO PINNED LINE NOT ACCESSED CACHE LINE CACHE BEFORE INTO THE CACHE BUT DO **NOT PINN REBOOT** YES 490 -CLEAR PINNED LINE AND NO **TIMER** PINN NEW CACHE LINE **EXPIRED** 

FIG. 4

YES

**END** 

**ACCESSED** 

460

470